

FIG. 1

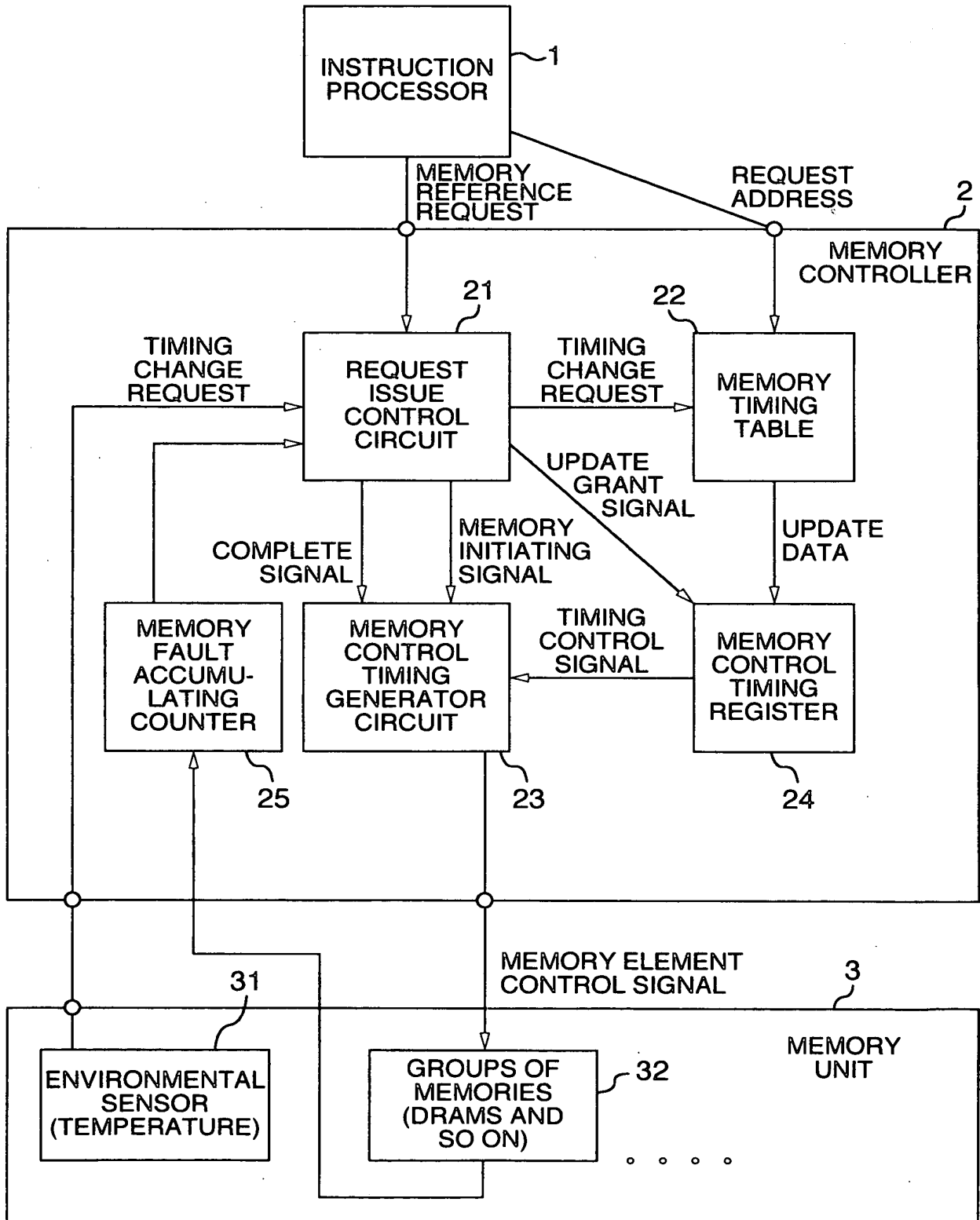


FIG. 2

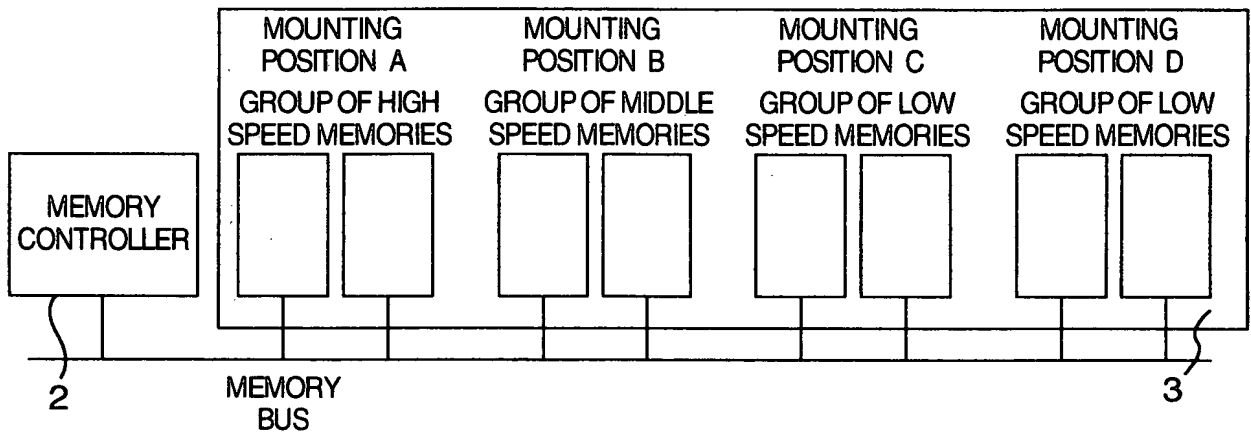


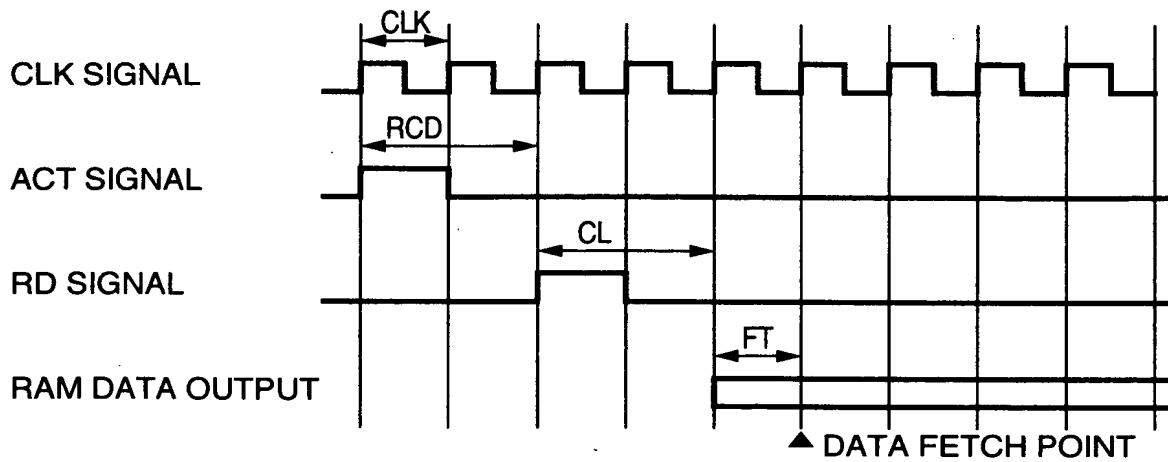
FIG. 3

| MOUNTING POSITION | MEMORY TIMING PARAMETERS | | | |
|-------------------|--------------------------|------|------|------|
| | CLK | RCD | CL | FT |
| A | 10ns | 20ns | 20ns | 10ns |
| B | 10ns | 20ns | 30ns | 10ns |
| C | 15ns | 30ns | 30ns | 10ns |
| D | 15ns | 30ns | 45ns | 15ns |

CONTENTS OF MEMORY TIMING TABLE

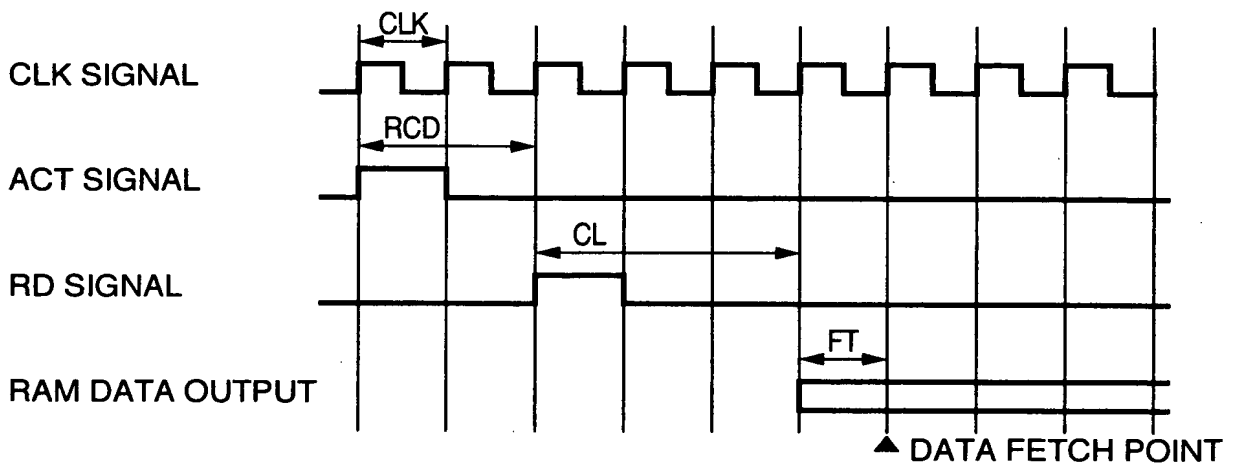
20250101 10:28:36

FIG. 4A



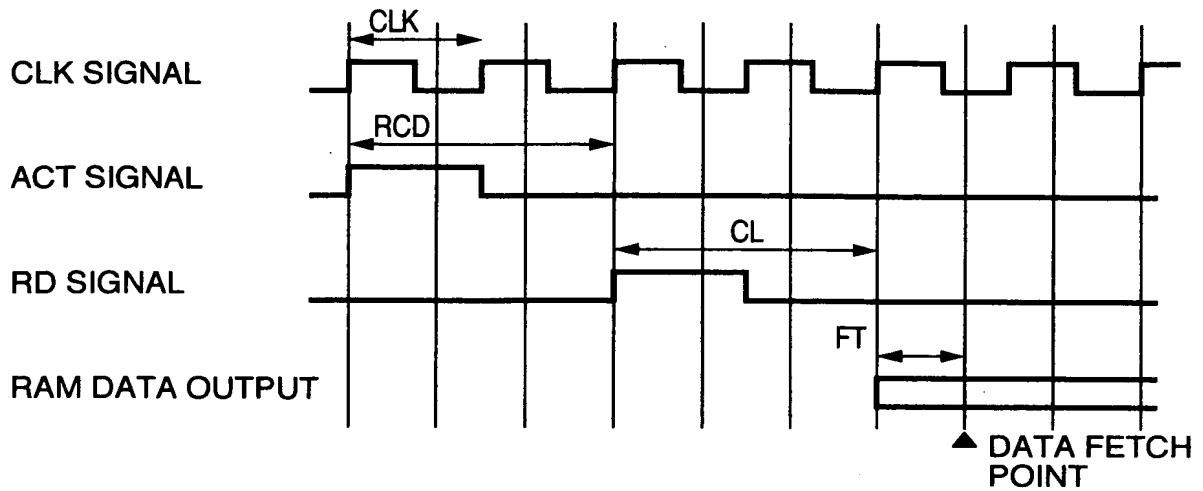
SDRAM OPERATION TIMING AT MOUNTING POSITION A

FIG. 4B



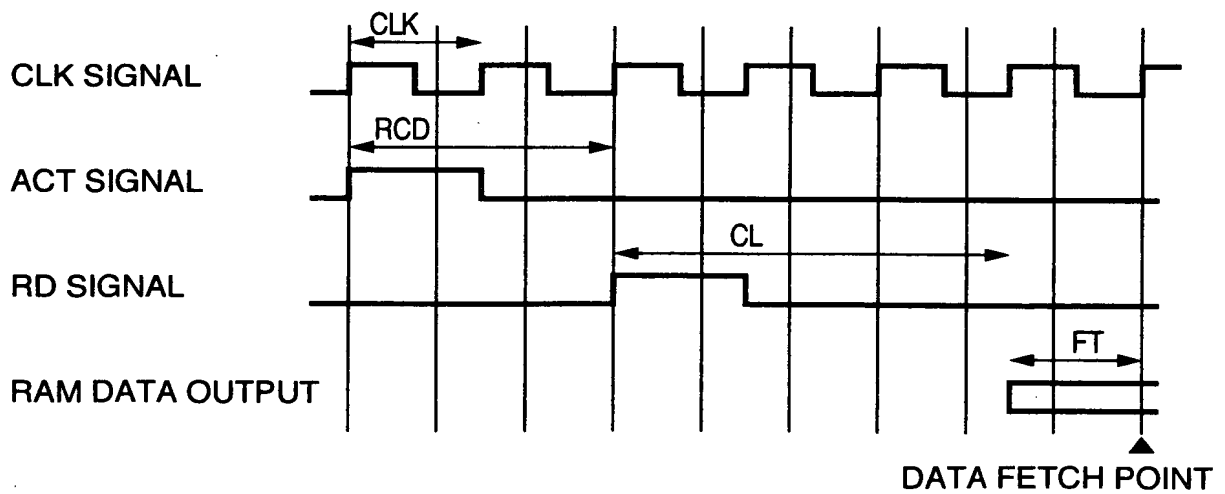
SDRAM OPERATION TIMING AT MOUNTING POSITION B

FIG. 5A



SDRAM OPERATION TIMING AT MOUNTING POSITION C

FIG. 5B



SDRAM OPERATION TIMING AT MOUNTING POSITION D